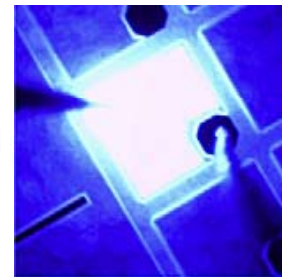
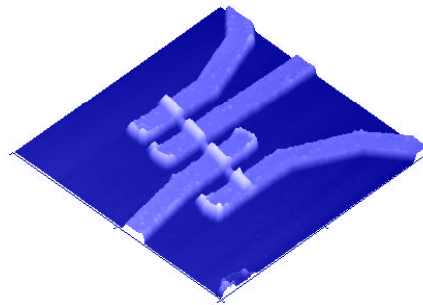
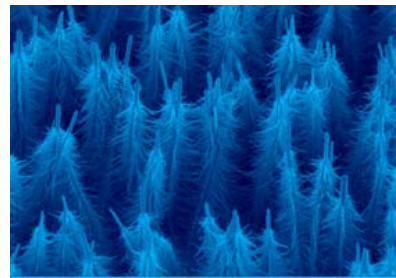


Semiconductor Physics

Devices and applications

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BME TTK, Nov. 20th, 2018
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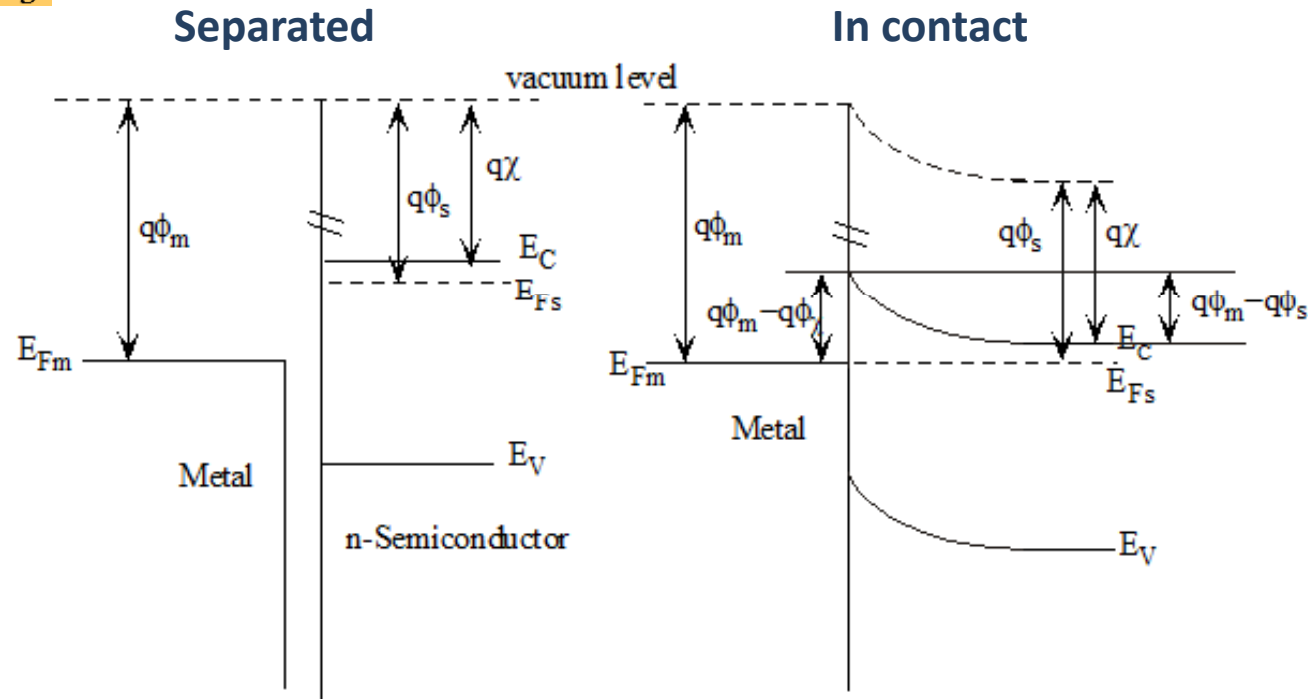


Outlook

- Metal-semiconductor junction
 - Ideal case: Schottky-Mott limit
 - Realistic metal-semiconductor interfaces
 - Current transport
 - Ohmic contact
- Metal-oxide-semiconductor field effect transistor (MOSFET)
 - MOS capacitance
 - MOS transistor
- Heterojunction devices
 - Band-gap engineering,
 - Blue Light Emitting Diode (LED)

Metal – n-type semiconductor contact in ideal case

$$\Phi_M > \Phi_S$$



Work function of semiconductor:

$$W = q\Phi_s = q\chi + q\Phi_n$$

Built-in potential (contact potential):

$$\Psi_{bl} = \Phi_M - \Phi_S$$

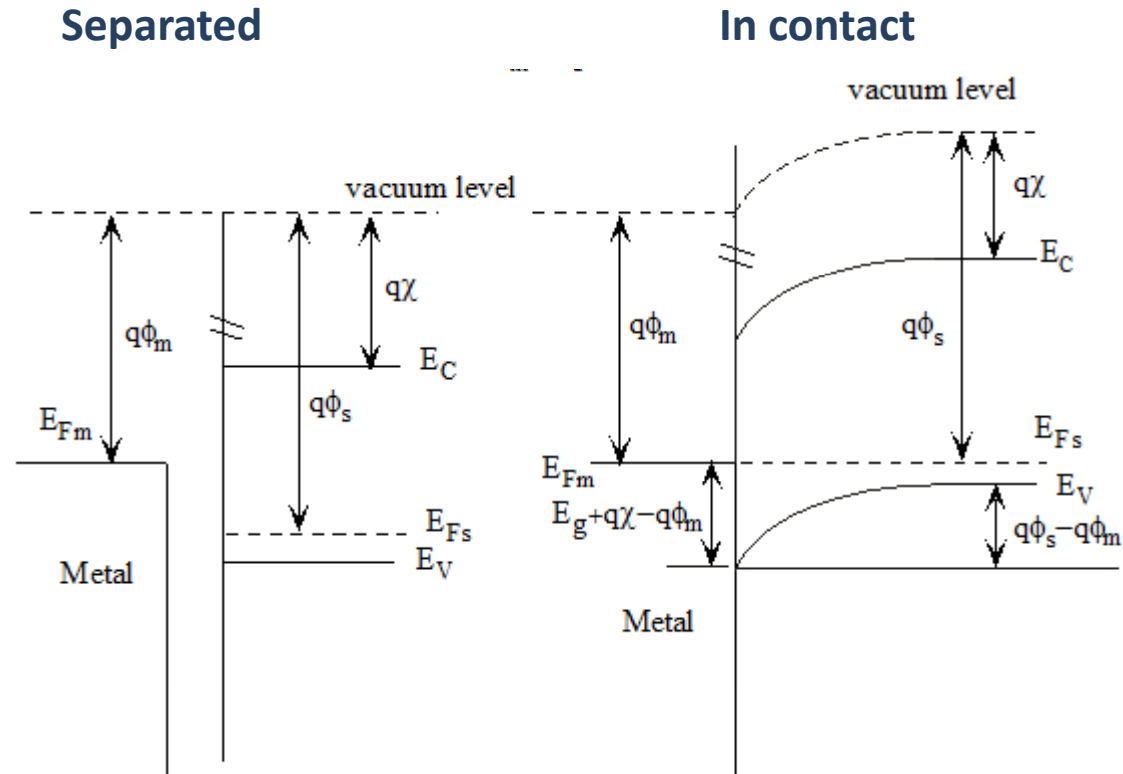
Schottky barrier height in Schottky-Mott limit (ideal case):

$$\Phi_{Bn0} = \Phi_M - \chi$$

➡ Φ_{Bn0} , i.e. rectifying properties can be tuned by metal selection (in ideal case)

Metal – p-type semiconductor contact in ideal case

$$\Phi_M < \Phi_S$$



Schottky barrier height:

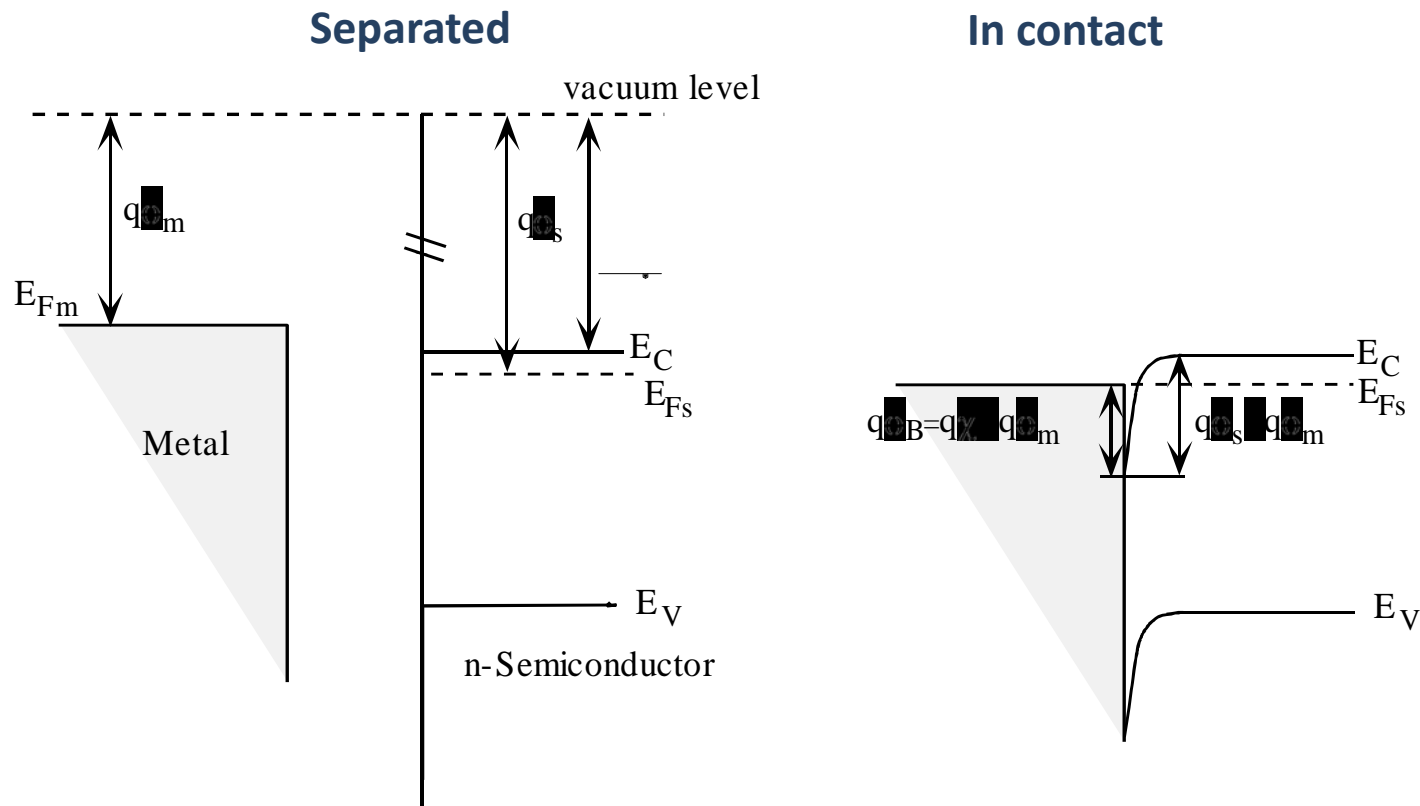
$$\Phi_{Bp0} = \epsilon_G - (\Phi_M - \chi)$$

for p- and n-type types of the same semiconductor and metal:

$$\Phi_{Bp0} + \Phi_{Bn0} = \epsilon_G$$

N-type semiconductor with large metal work function

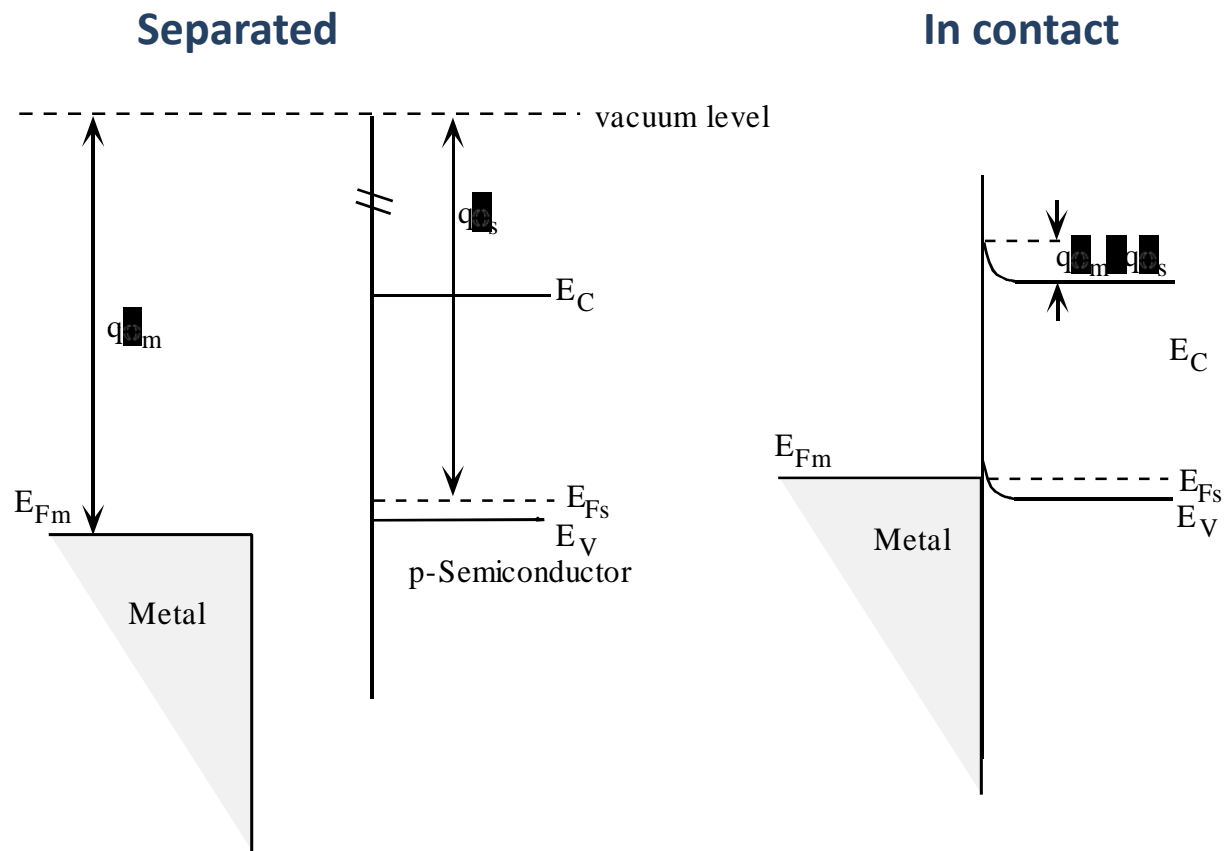
$$\Phi_M < \Phi_S$$



- 2 Dimensional Electron Gas (2DEG) at the interface
- Leads to Ohmic (non-rectifying) behavior automatically
- Requires pair selection of semiconductor and metal which is not practical

P-type semiconductor contact in ideal case

$$\Phi_M > \Phi_S$$



- 2 Dimensional Hole Gas (2DHG) at the interface
- It would result in an automatic Ohmic contact formation
- But, it's nearly impossible, especially for wide bandgap semiconductors

Metal-semiconductor contact: ideal case

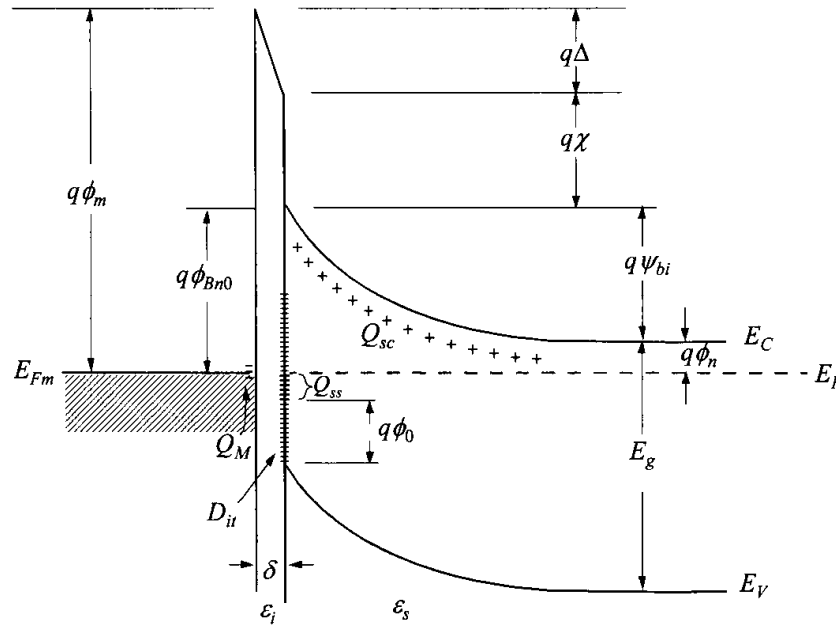
Table 1: Metal Workfunctions

Material	Φ_m (eV)
Mg	3.7
Al	4.3
Ti	4.3
Cr	4.5
W	4.6
Ag	4.64
Au	5.1
Pd	5.1
Ni	5.15
Pt	5.7

Table 2: Electron affinity (χ) of various semiconductors.

Material	Electron affinity χ (eV)
Si	4
Ge	4.03
GaAs	4.07
$\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$	4.5
InAs	4.9
InP	4.38
GaSb	4.06
GaN	4.1

Realistic Schottky contacts



- ϕ_m = Work function of metal
- ϕ_{Bn0} = Barrier height (without image-force lowering)
- ϕ_0 = Neutral level (above E_V) of interface states
- Δ = Potential across interfacial layer
- χ = Electron affinity of semiconductor
- ψ_{bi} = Built-in potential
- δ = Thickness of interfacial layer
- Q_{sc} = Space-charge density in semiconductor
- Q_{ss} = Interface-trap charge
- Q_M = Surface-charge density on metal
- D_{it} = Interface-trap density
- ϵ_i = Permittivity of interfacial layer (vacuum)
- ϵ_s = Permittivity of semiconductor

If $D_{it} \rightarrow \infty$: Fermi level pinning

$$\phi_{Bn0} = \epsilon_G - \Phi_0$$

If $D_{it} \rightarrow 0$: Schottky-Mott limit

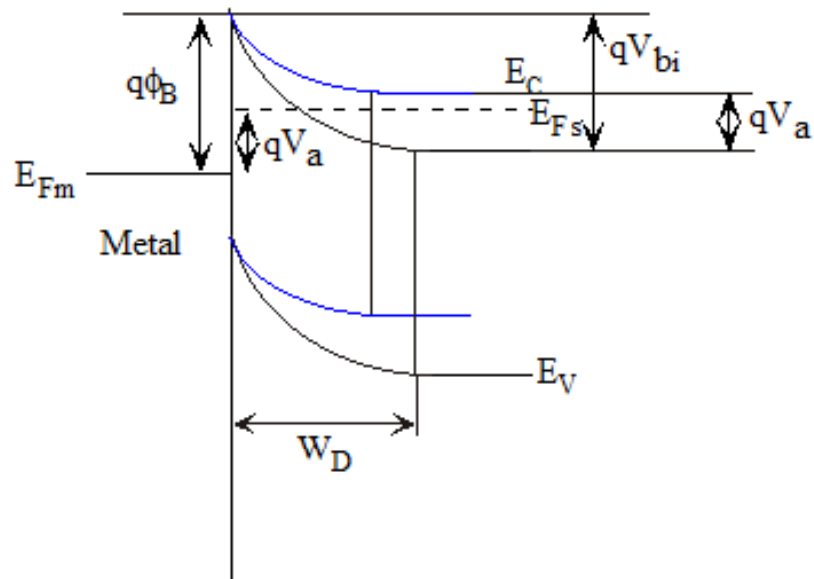
$$\phi_{Bn0} = \Phi_M - \chi$$

In practice: $\epsilon_G - \Phi_0 < \phi_{Bn0} < \Phi_M - \chi$

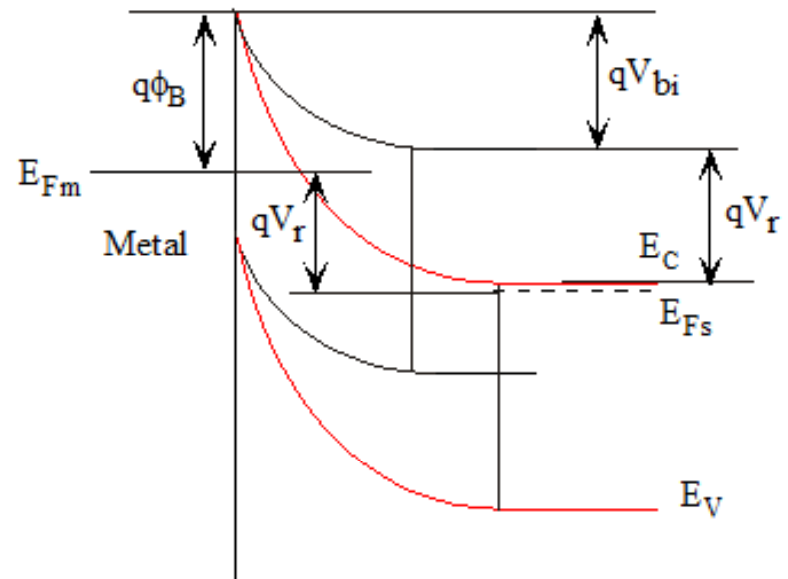
$C = \frac{d\phi_{Bn0}}{d\phi_m}$ depends on the semiconductor family

Metal – n-type semiconductor with bias

Forward bias

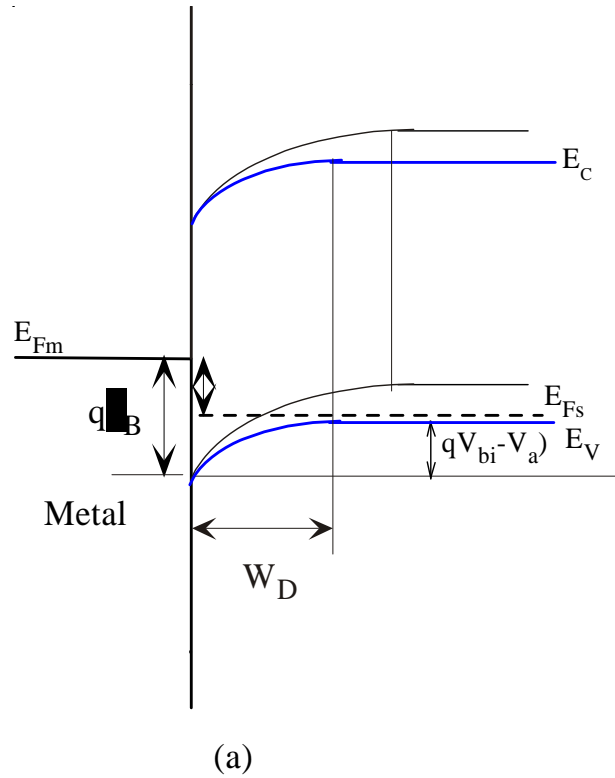


Reverse bias

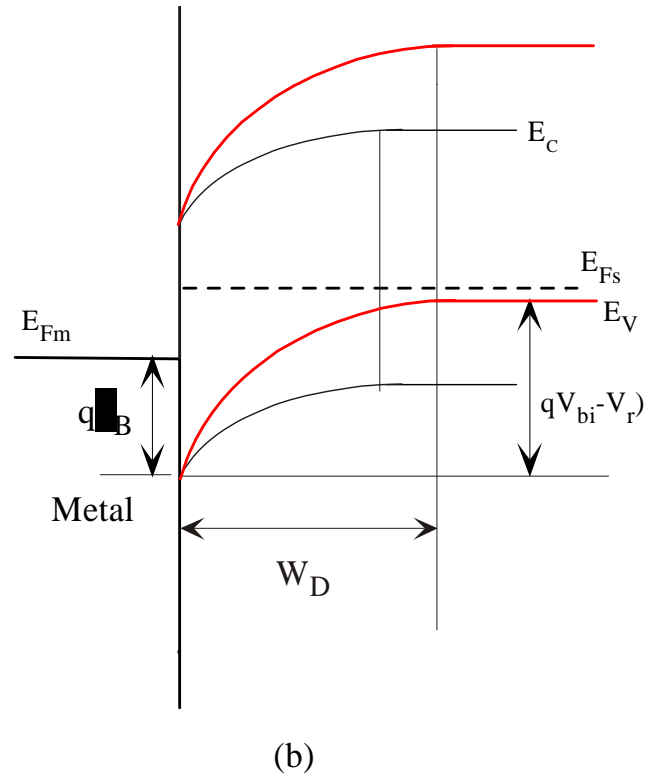


Metal – p-type semiconductor with bias

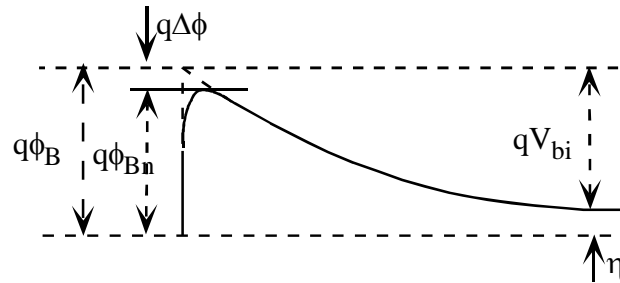
Forward bias



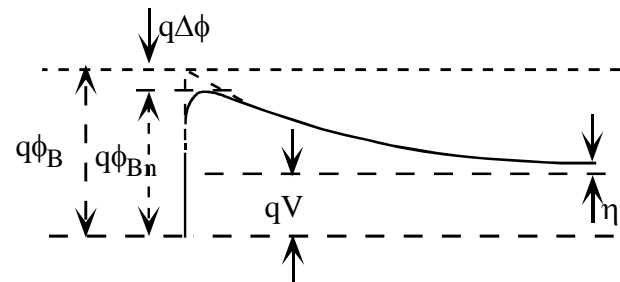
Reverse bias



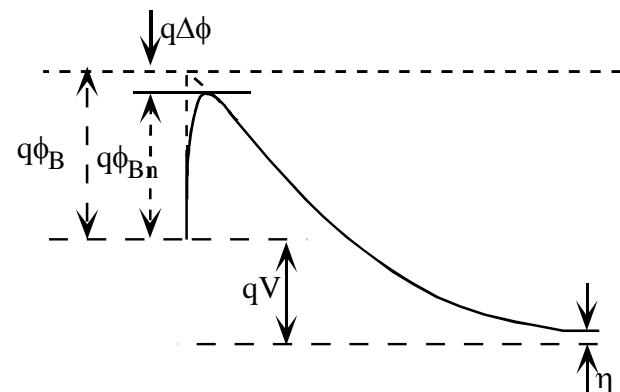
Metal – n-type, conduction band only



a). Equilibrium



b). Forward Bias



c). Reverse Bias

- Schottky barrier height is lowered by $q\Delta\Phi$ due to the image charge effect!

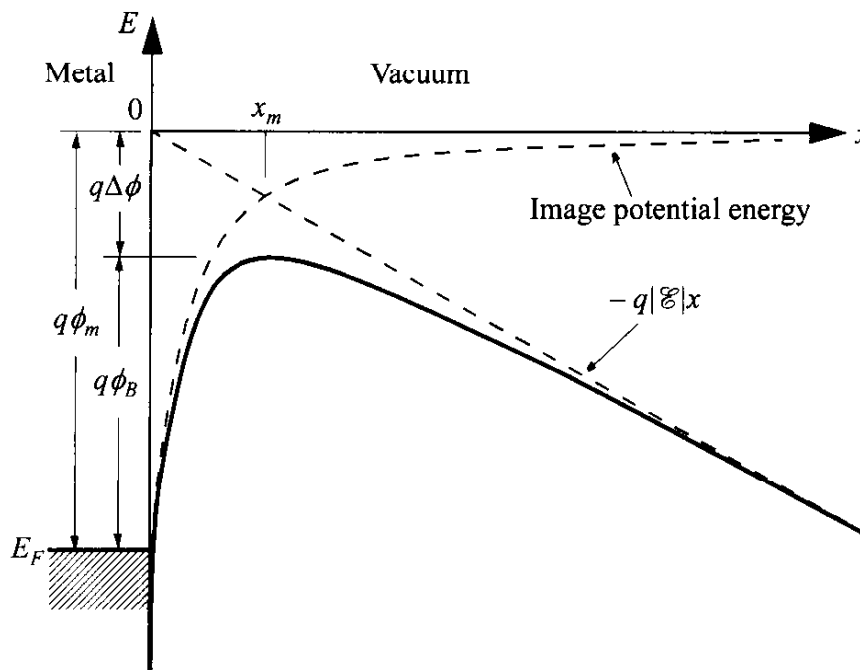
Image-Force Lowering (Schottky-barrier lowering)

Metal-vacuum system

Attractive force toward metal (image force): $F = \frac{-q^2}{4\pi\epsilon_0(2x)^2}$

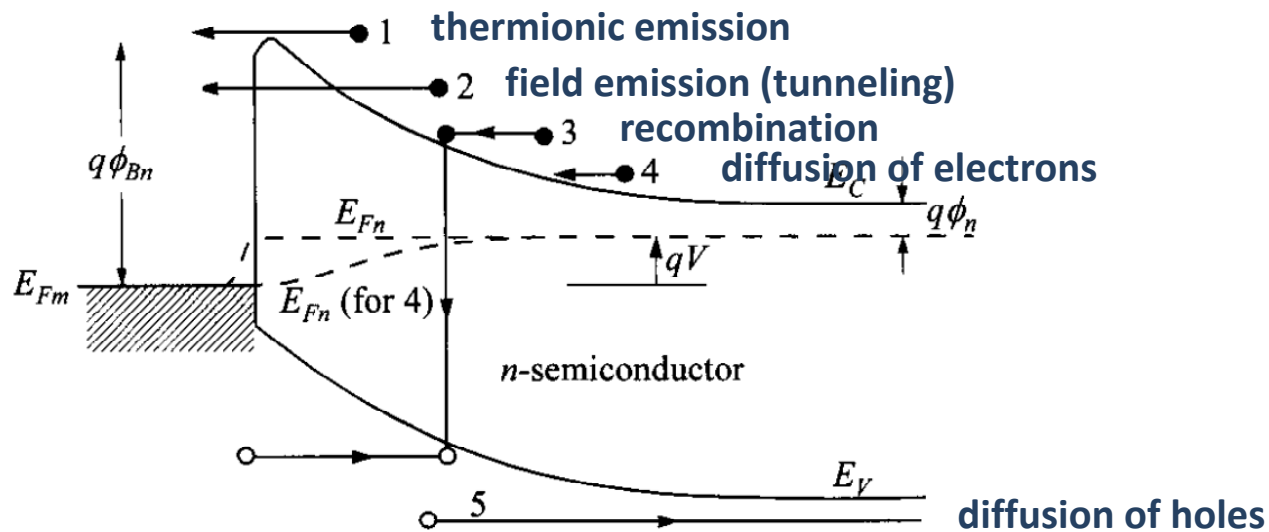
Potential energy: $E = \frac{-q^2}{16\pi\epsilon_0 x} - q|\epsilon|x$

Location of the energy maximum (dE/dx=0): $x_m = \sqrt{\frac{q}{16\pi\epsilon_0|\epsilon|}}$ $\Delta\Phi = 2|\epsilon|x_m$



Concept is also valid for metal-semiconductor system.

Current transport on Schottky contact



(1) Thermionic Emission (TE). For lightly or moderately doped semiconductors, $N_d < \approx 10^{17} \text{ cm}^{-3}$, the depletion region is relatively wide. It is, therefore, nearly im-possible for electrons to tunnel through the barrier unless aided by defects, which are con-sidered not to exist in this ideal picture.

(2) Thermionic-Field Emission (TFE). For intermediately doped semiconductors, $\approx 10^{17} < N_d < \approx 10^{18} \text{ cm}^{-3}$, the depletion region is not sufficiently thin to allow direct tunneling of carriers that are more or less in equilibrium. This process requires some energy gain from the bias sufficient to raise the electron energy to a value E_m where the barrier is sufficiently thin for tunneling.

(3) Field Emission (FE). In heavily doped semiconductors, $N_d > \approx 10^{18} \text{ cm}^{-3}$, the depletion region is narrow even for cold and cool electrons at the bottom of the conduction band or at the Fermi level, the latter is for degenerate semiconductors, and direct electron tunneling from the semiconductor to the metal is allowed

Thermionic emission regime

$$J_{te} = J_{te0} \left(e^{-\frac{qV}{kT}} - 1 \right) \quad \text{with} \quad J_{te0} = A^* T^2 e^{-\frac{q(\phi_B - \Delta\phi)}{kT}}$$

- J_{te0} : saturation current
- A^* : effective Richardson constant

Richardson constant for free space:

$$A_{free}^* = \frac{4\pi q k^2 m_0}{h^3}$$

which equals to 120 (Acm⁻²K⁻²)

Richardson for n-type constant is:

$$A^* = A_{free}^* \left(\frac{m_e^*}{m_0} \right)$$

Richardson for p-type constant is:

$$A^* = A_{free}^* \left(\frac{m_{hh}^*}{m_0} \right)$$

Experimental expression:

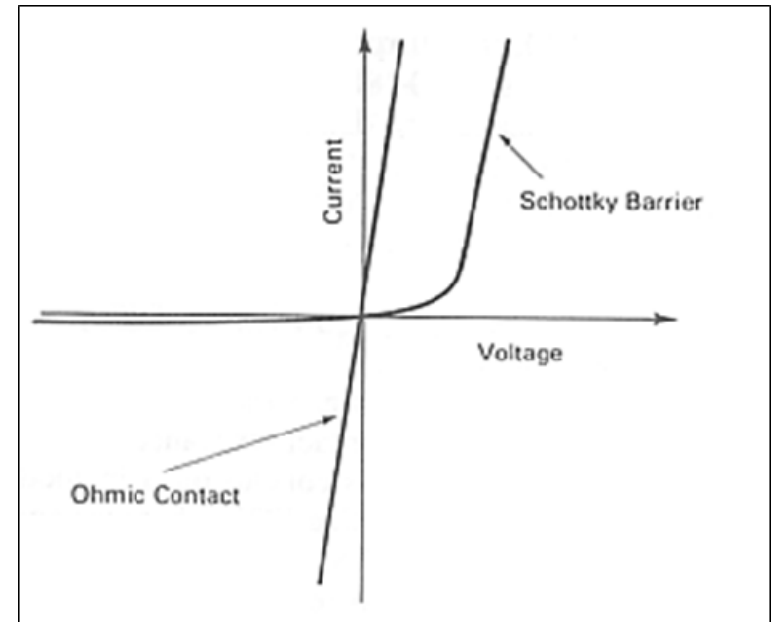
$$J = J_0 \left[\exp\left(\frac{qV}{\eta kT}\right) - 1 \right]$$

Where η is the ideality factor: 1 for purely thermionic, >1 for thermionic field (TFE) emission

Ohmic contact

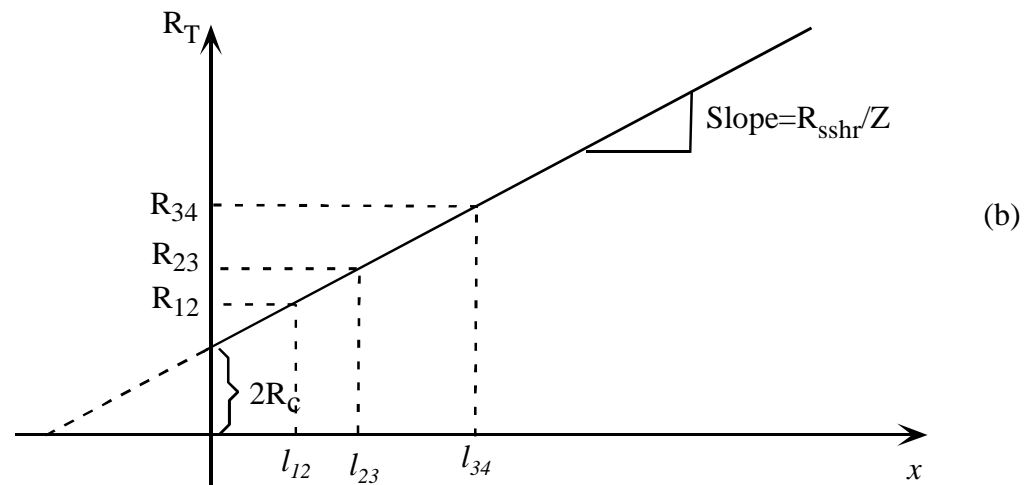
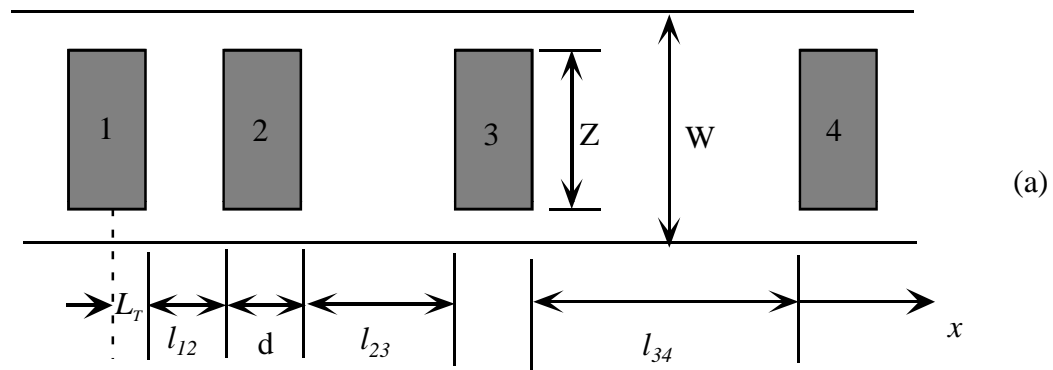
- Metal-semiconductor contact with negligible junction resistance
- Low resistance contact is a must for all of the semiconductor devices
- By minimizing the Schottky barrier height or/and applying a highly doped intermediate layer

$$R_c = \left(\frac{\partial V}{\partial j} \right)_{V=0}$$



Transmission Line Method (TLM)

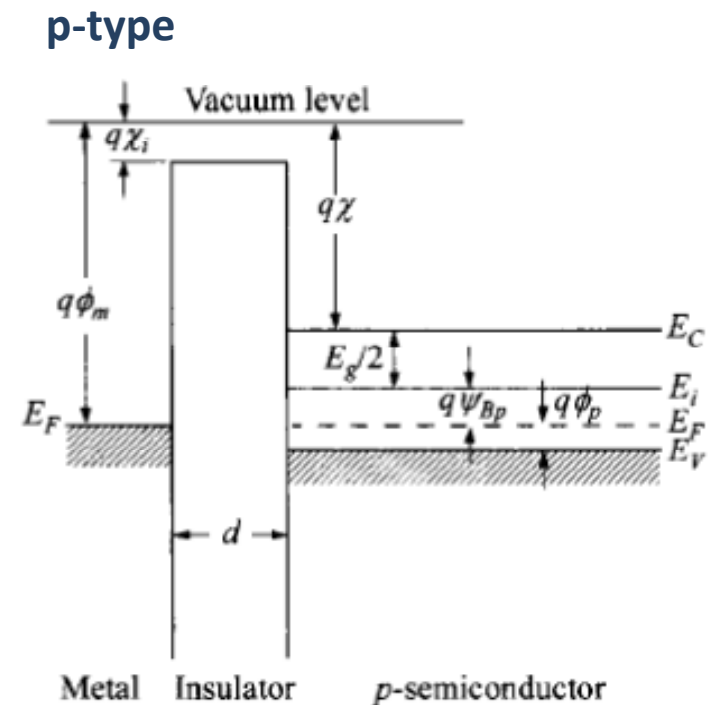
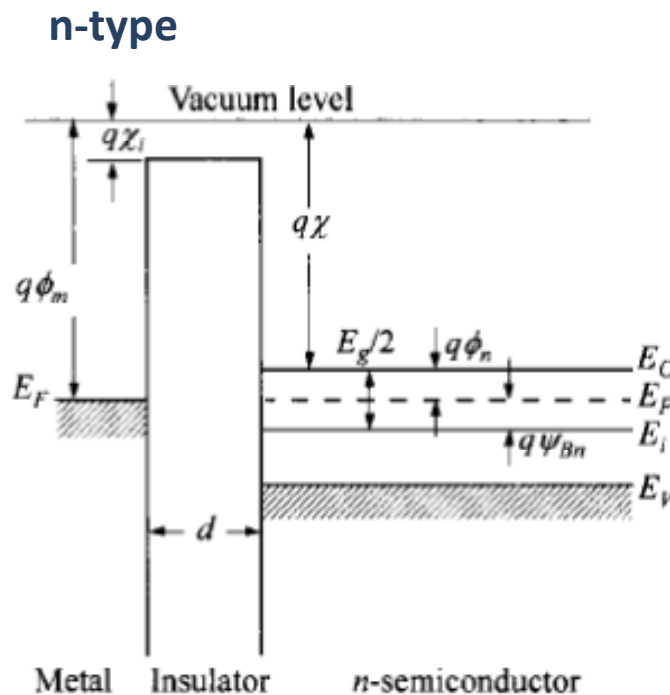
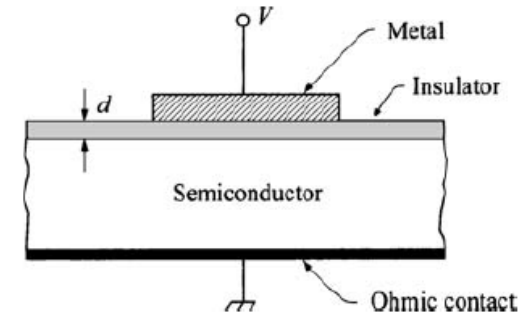
- To determine contact resistivity (Ωcm^2) and sheet resistance (Ω/\square)



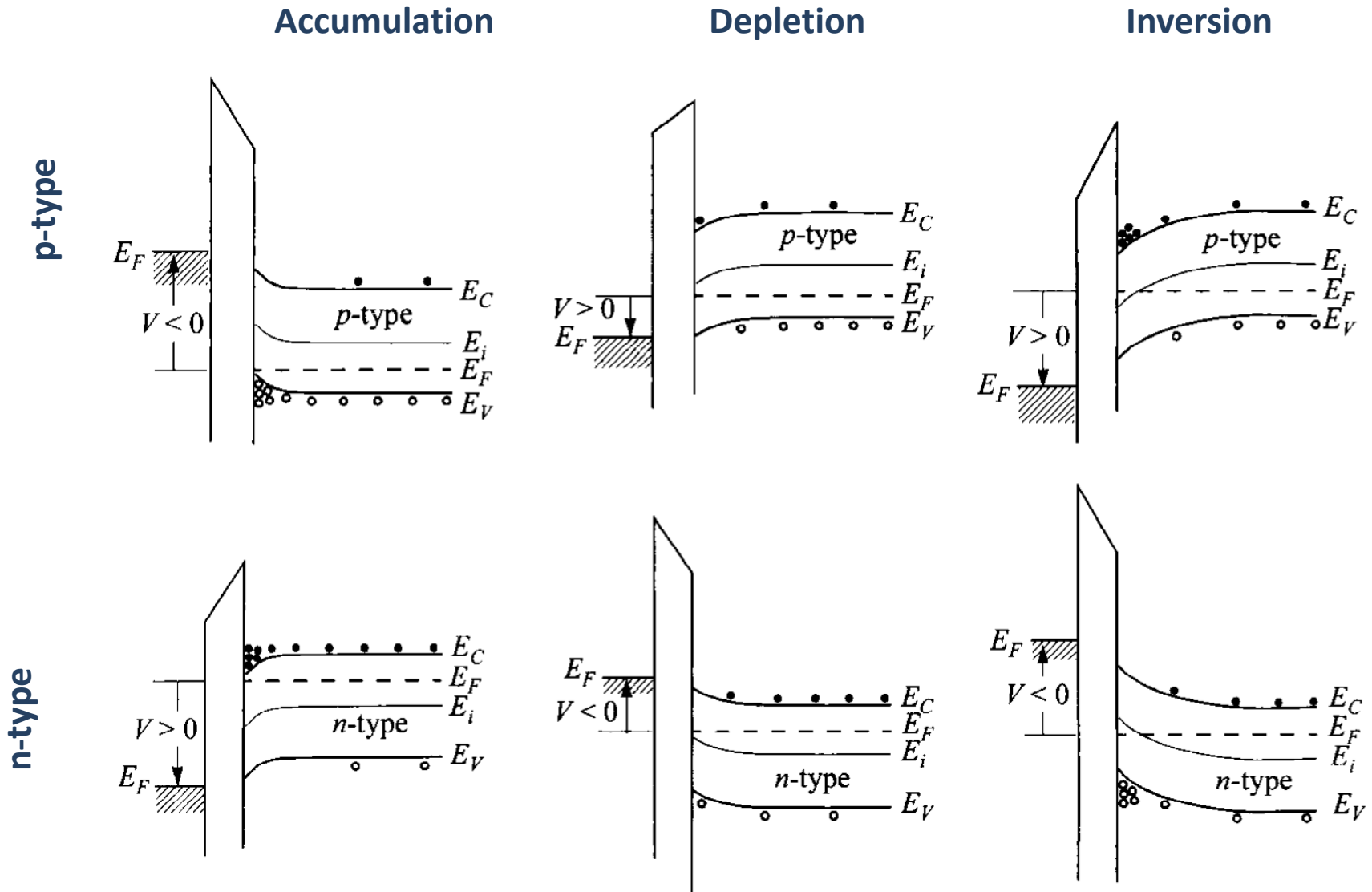
Metal-Insulator-Semiconductor (MIS) capacitor

Idealization:

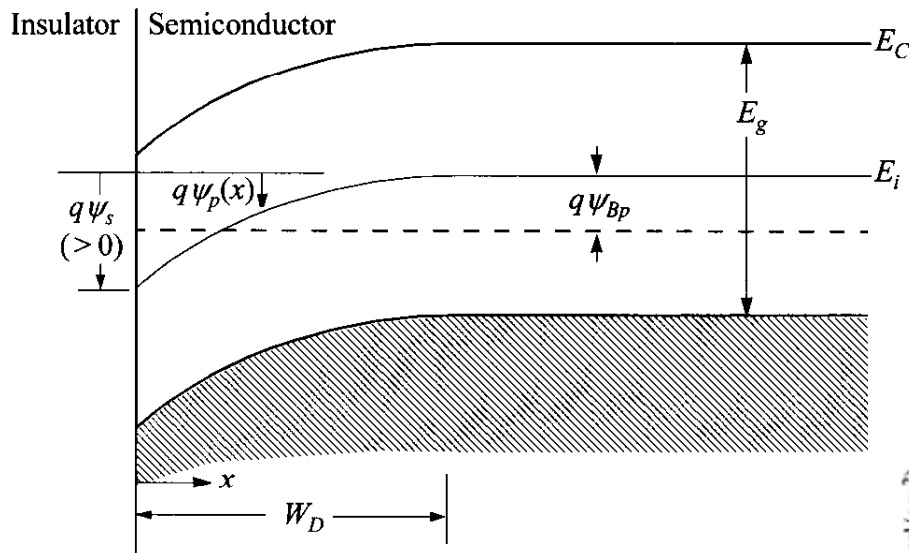
- Charges from the semiconductor and metal with equal but opposite sign; no interface trap, oxide charge etc.
- No current transport through the insulator
- For the sake of simplicity: $\phi_{MS}=0$ (flat band)



Metal-Insulator-Semiconductor capacitor



Metal-Insulator-Semiconductor capacitor



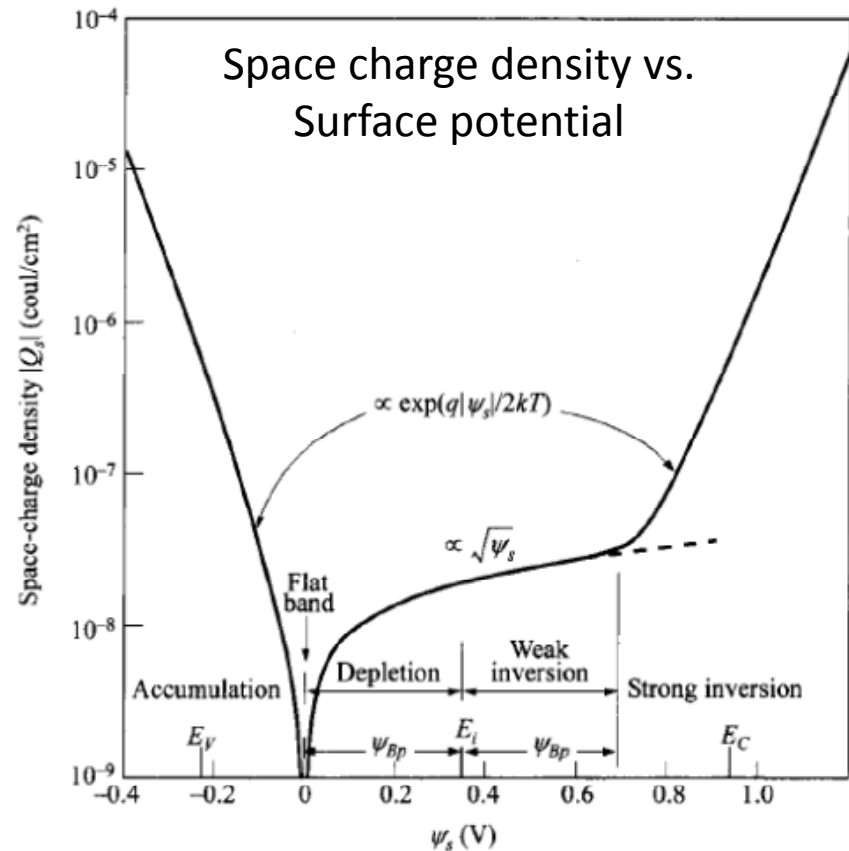
$$\psi_p(x) \equiv - \frac{[E_i(x) - E_i(\infty)]}{q}$$

By solving the Poisson equation

$$\frac{d^2 \psi_p}{dx^2} = - \frac{\rho(x)}{\epsilon_c}$$

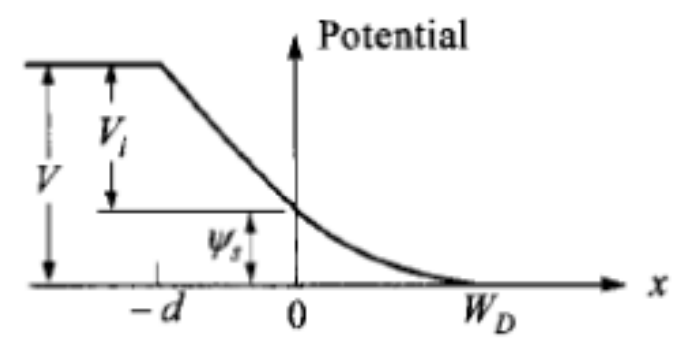
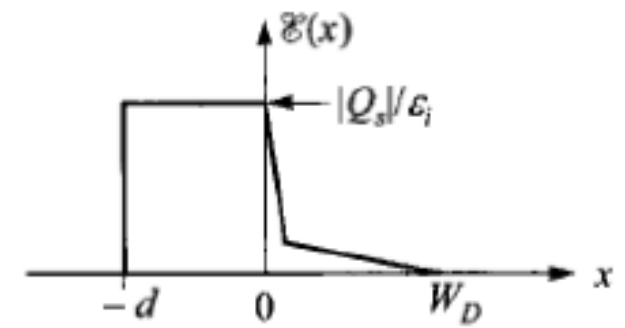
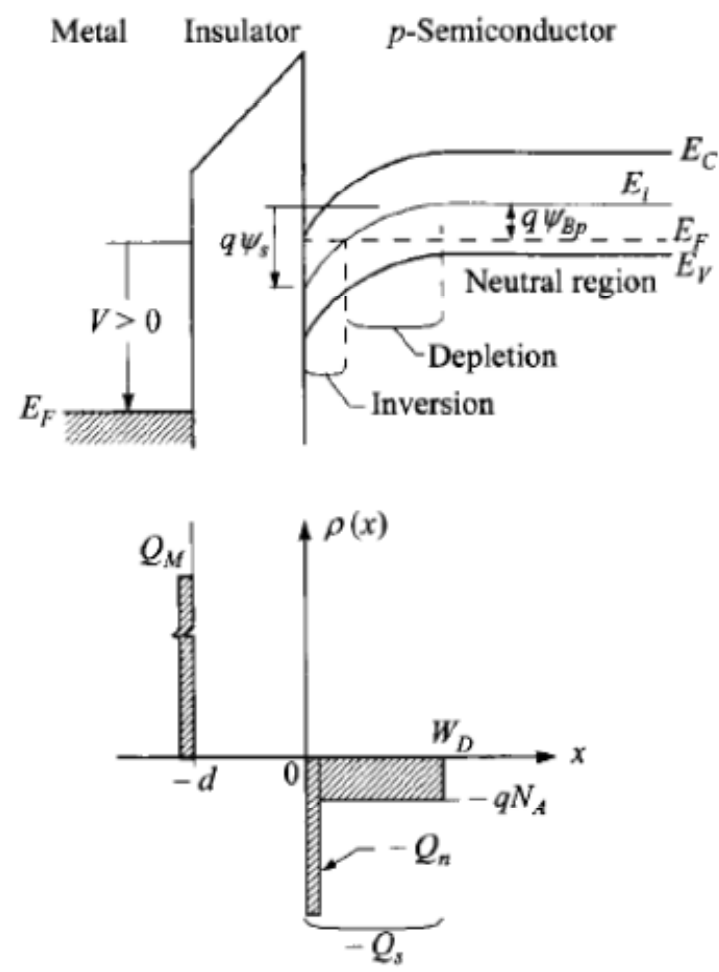


$$\rho(x) = q(N_D^+ - N_A^- + p_p - n_p),$$

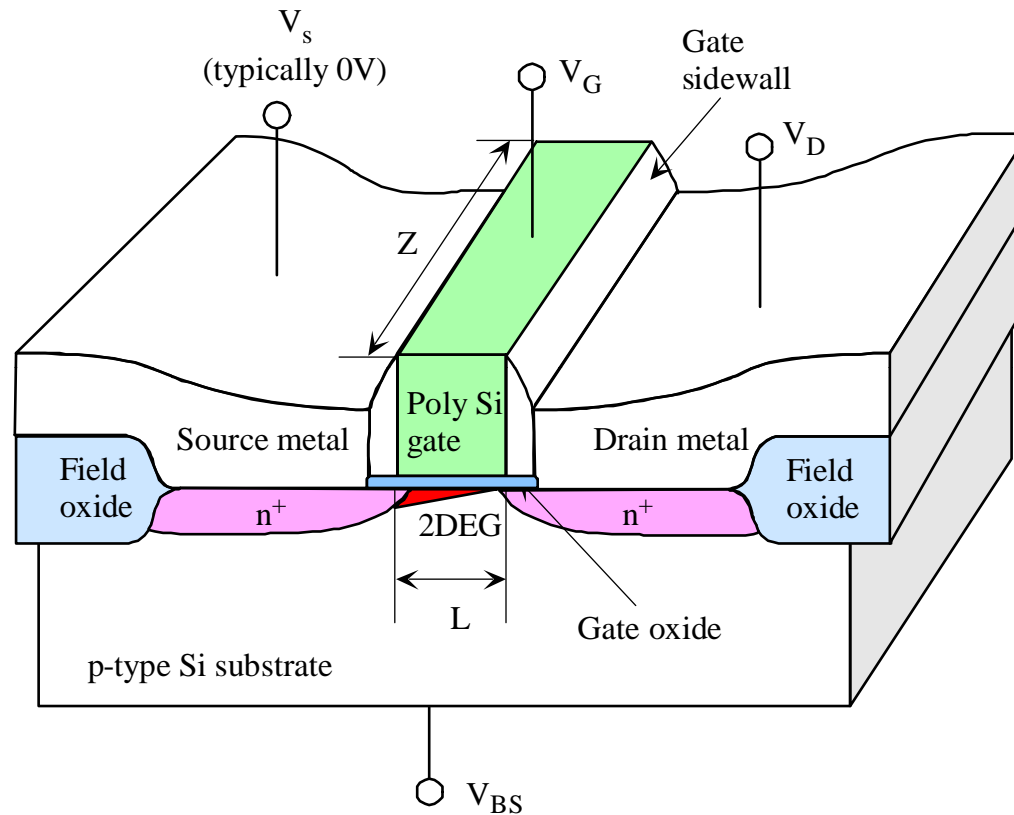


Metal-Insulator-Semiconductor capacitor

P-type case in strong inversion



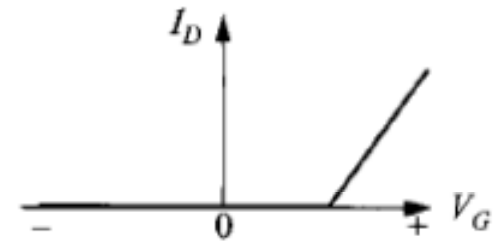
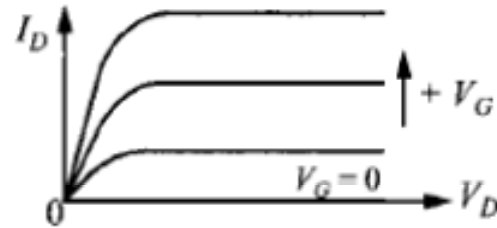
Metal-Oxide-Semiconductor field-effect transistor (MOSFET)



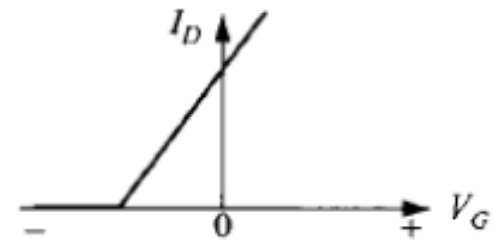
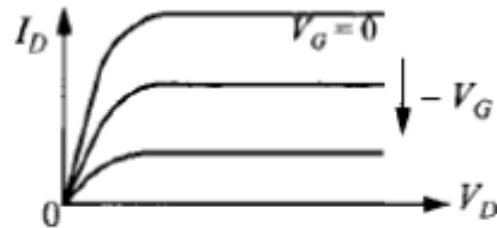
- **Source** terminal supplies charge carriers
- **Drain** terminal sinks charge carriers
- **Gate** terminal controls the conduction between source and drain
- **Bulk** terminal on the substrate

Versions of MOSFET

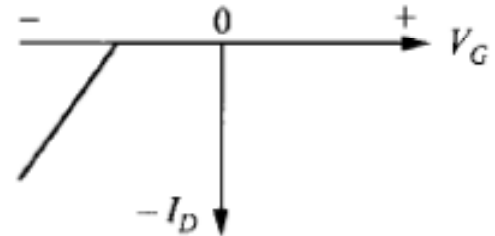
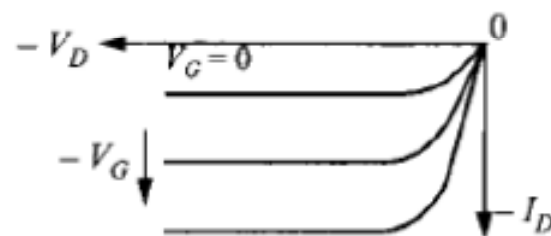
n-channel
Enhancement-mode
(Normally-off)



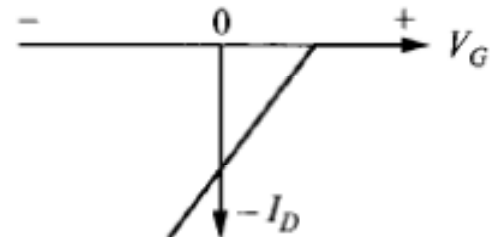
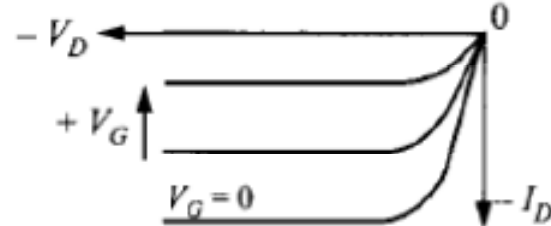
n-channel
Depletion-mode
(Normally-on)



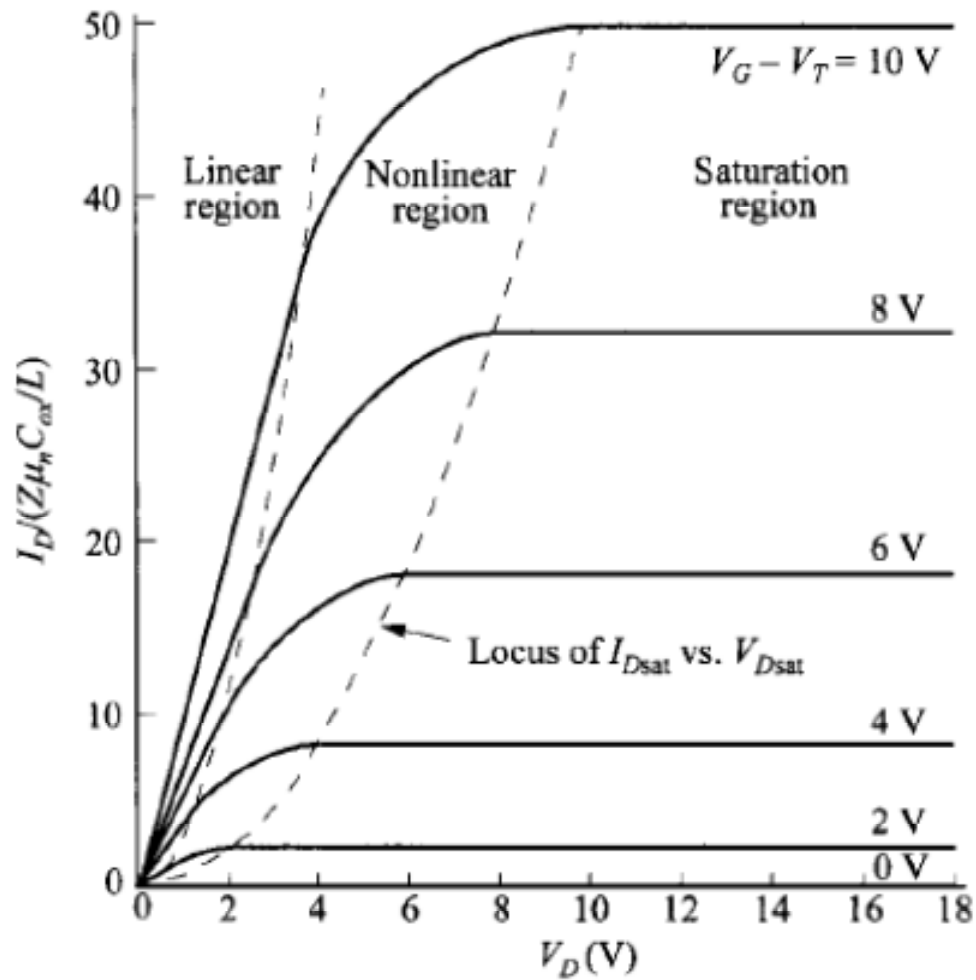
p-channel
Enhancement-mode
(Normally-off)



p-channel
Depletion-mode
(Normally-on)

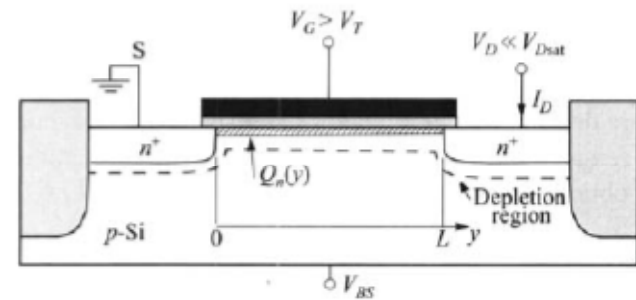


Idealized drain current characteristics for n-channel, enhancement type transistor

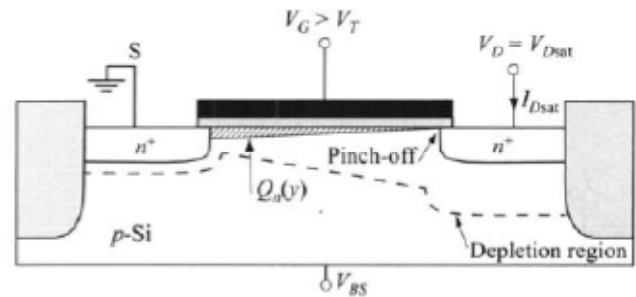


Idealized drain current characteristics for n-channel, enhancement type transistor

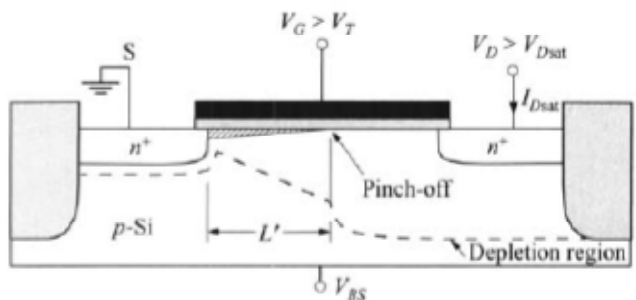
linear region for $V_G > V_T$
and $V_D < (V_G - V_T)$;



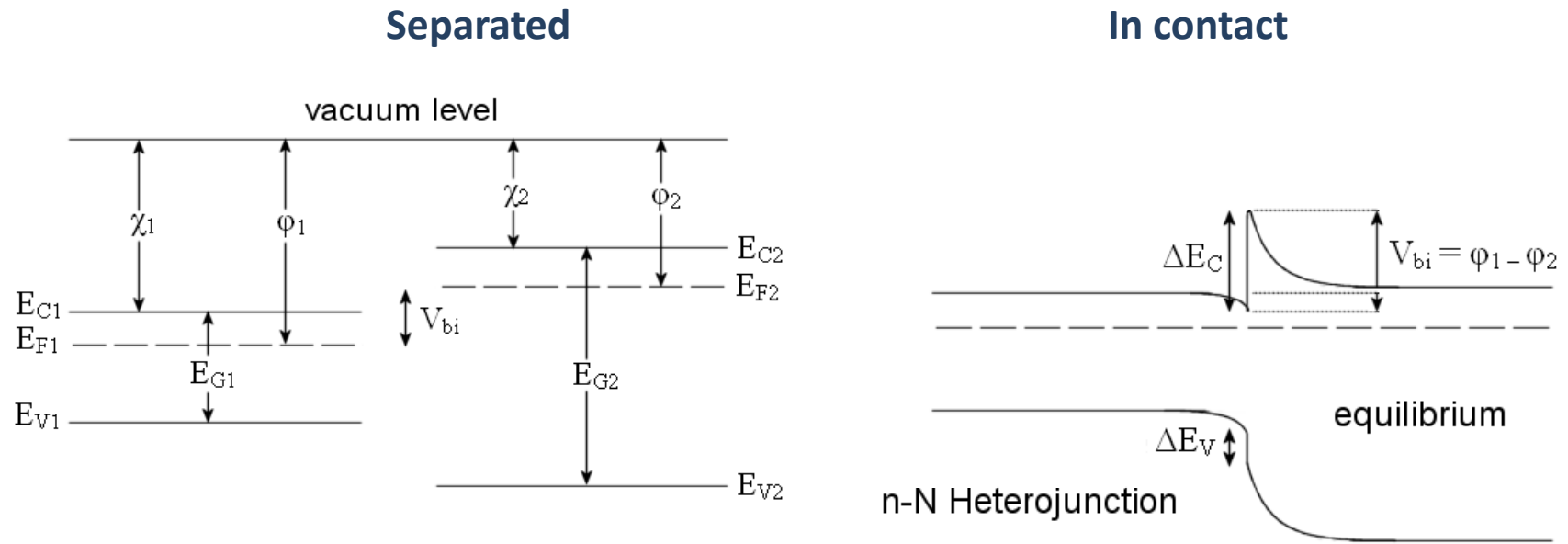
onset of saturation at pinch-off,
 $V_G > V_T$ and $V_D = (V_G - V_T)$;



strong saturation, $V_G > V_T$
and $V_D > (V_G - V_T)$.



Semiconductor heterojunctions



Band offsets calculated by Anderson's rule:

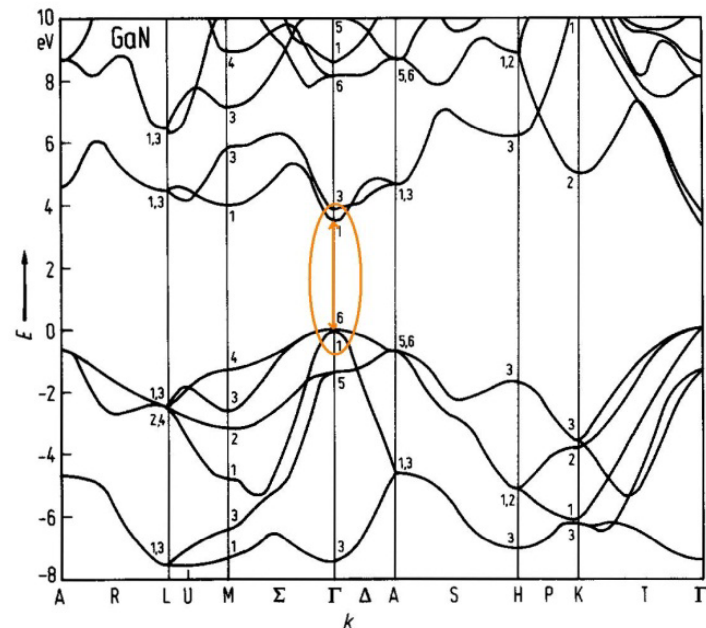
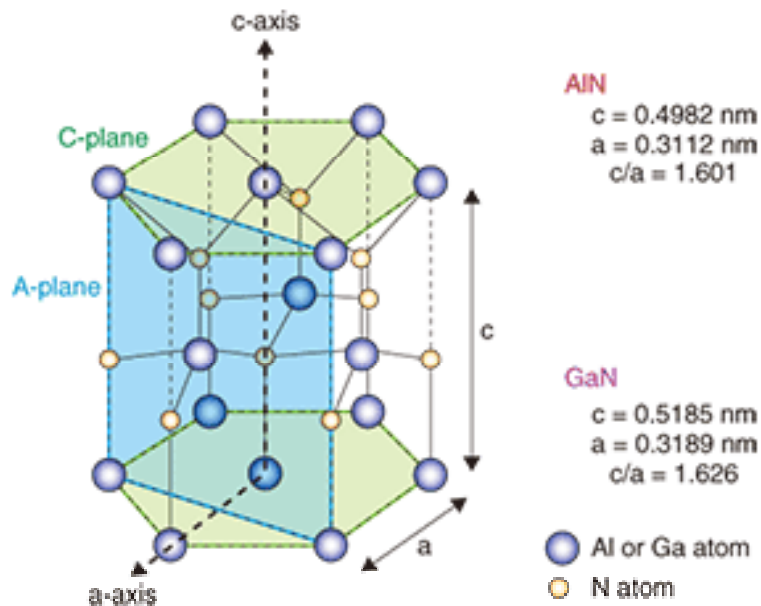
$$\Delta \epsilon_c = (\chi_2 - \chi_1)$$

$$\Delta \epsilon_v = (\chi_1 + \epsilon_{G1}) - (\chi_2 + \epsilon_{G2})$$

One example on heterojunction and band engineering: multi-quantum well LED

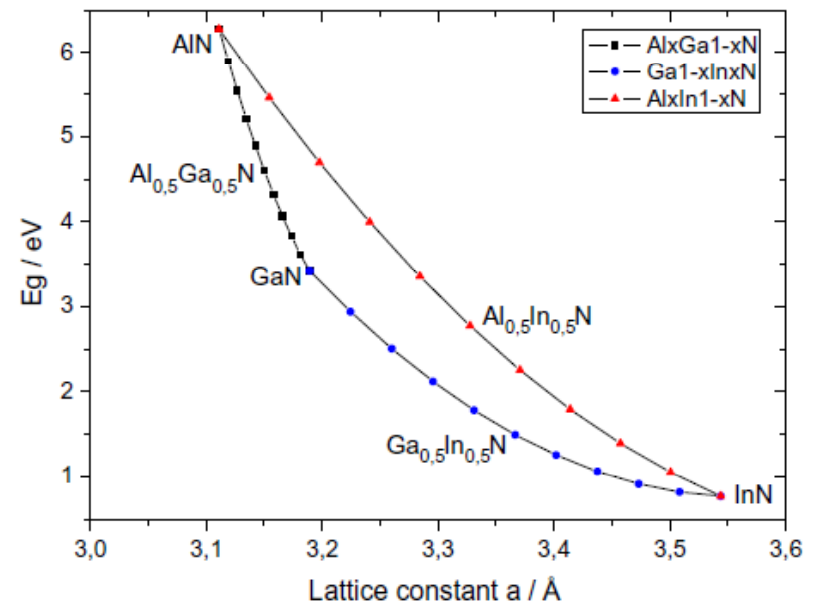
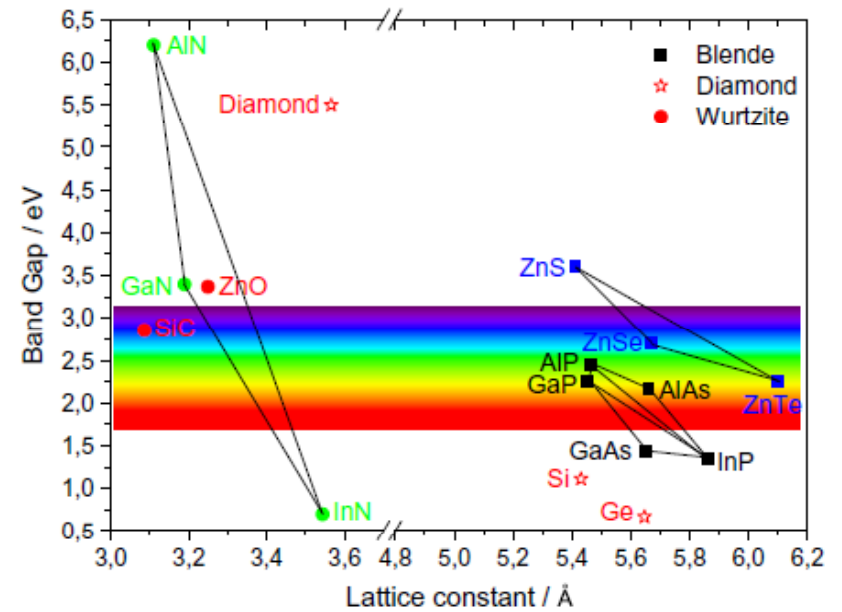
GaN, as LED and LD material

- Stable crystal structure: hexagonal wurtzite (C_{6v}^4 -P6₃mc, hcp)
- Wide direct bandgap (3,4 eV; 365 nm; 300 K-n)
- Stable n- and p-type doping are established (with Si and Mg)



GaN

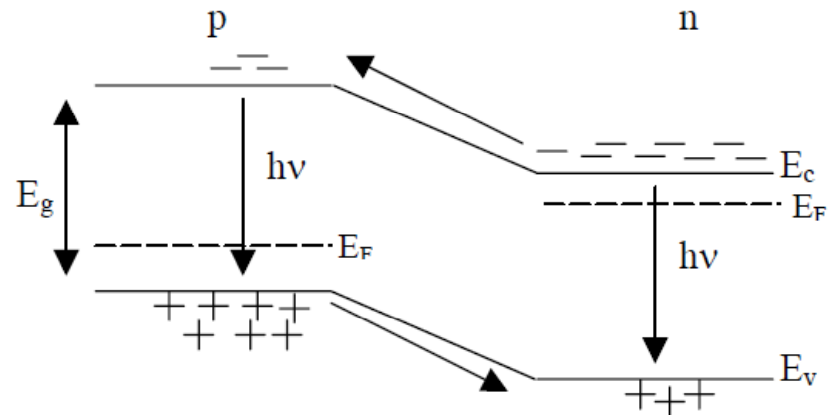
- Epitaxial layer on sapphire, SiC, GaN or even Si substrate up to 8"
- contains high density of threading dislocation
- Bandgap engineering: AlGaN, InGaN



Light Emitting Diode

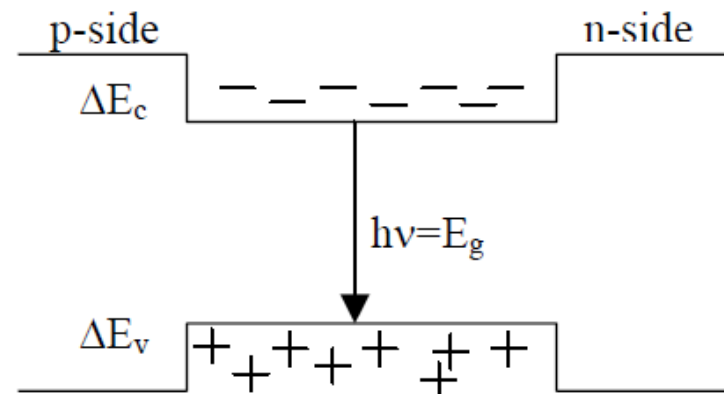
Simple LED: p-n homojunction

- low degree of spontaneous radiating recombination

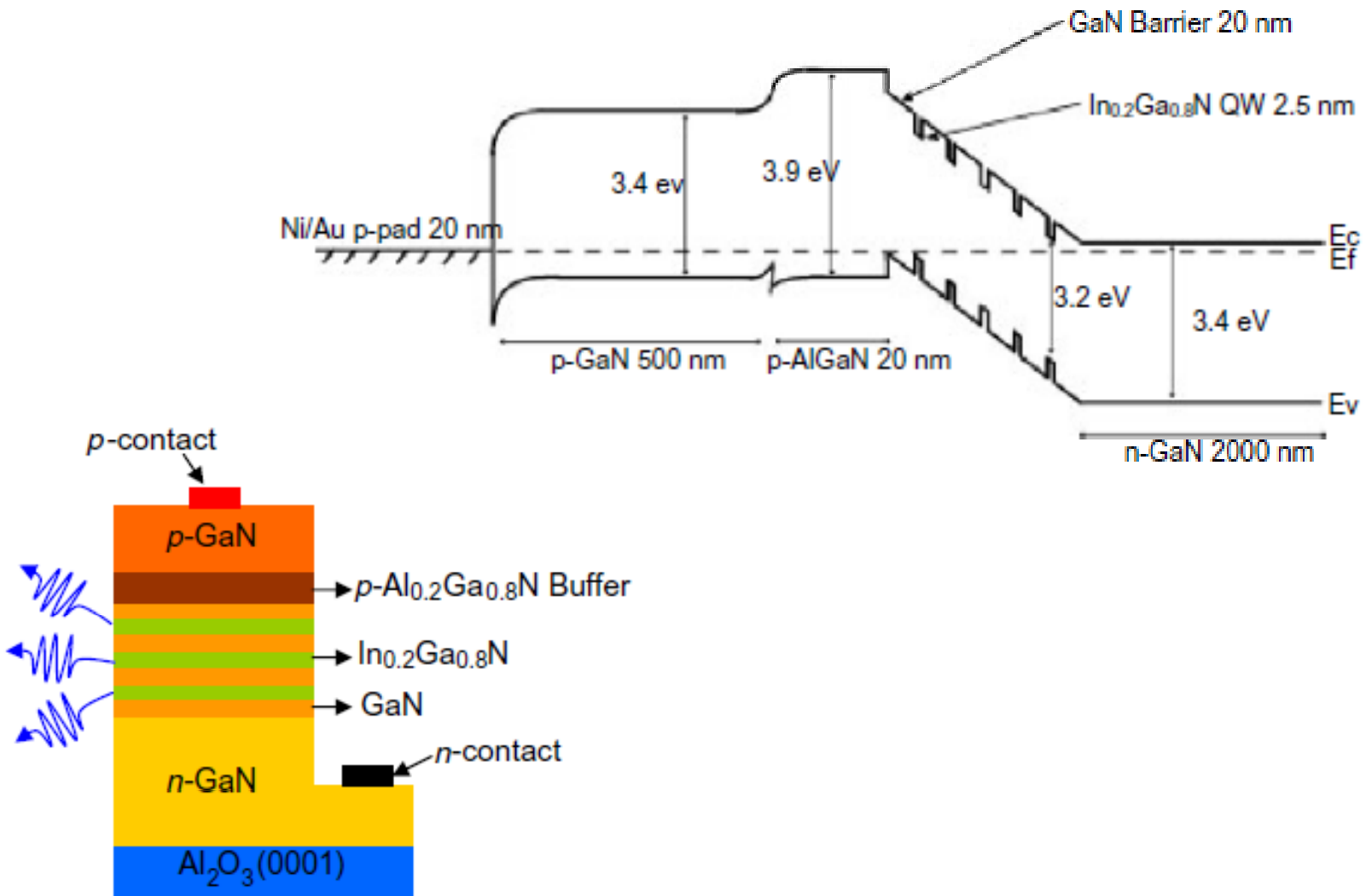


Quantum well by heterojunctions

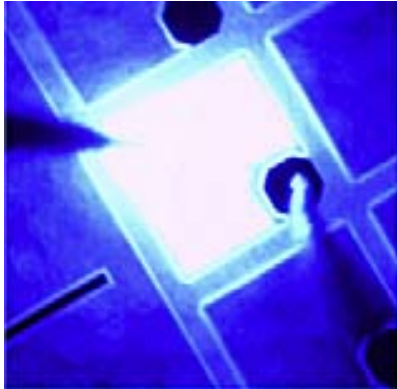
- High radiating recombination rate: $R=B \cdot n \cdot p$



III-N „multiquantumwell“ LED structure



III-N LEDs



LED Architecture and Design Concepts

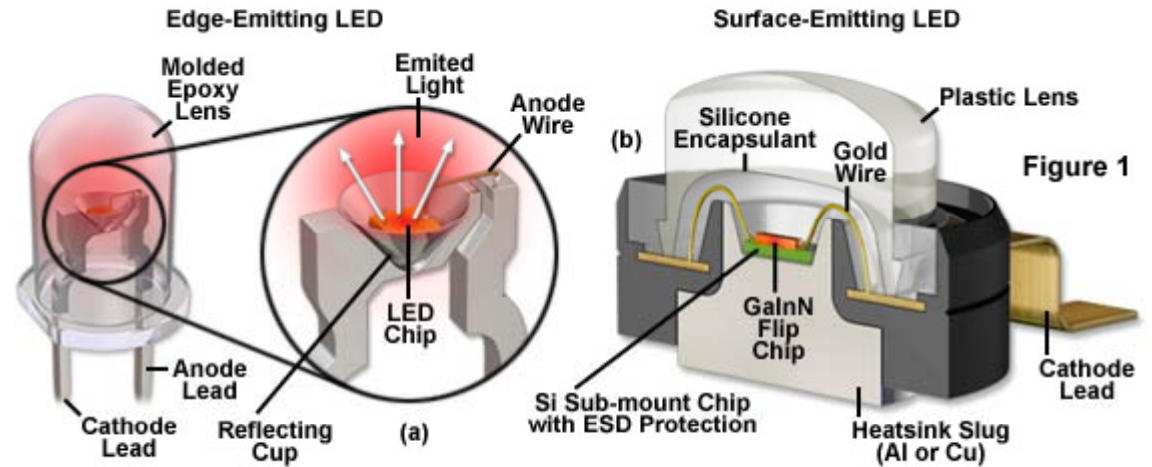


Figure 1

